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CASE 485FN **ISSUE B** A в A3 A1 DETAIL A TOP VIEW DETAIL A -// 0.10 C 0.08 C SEATING PLANE NOTE 4 С SIDE VIEW 15X L ບພູ່ແກ ⊃ 13 e 14 15 15X h 0.10 C A B \$ 0.05 C BOTTOM VIEW NOTE 3 GENERIC **MARKING DIAGRAM*** XXXXXX XXXXXX ALYW= XXXXXX = Specific Device Code = Assembly Location А = Wafer Lot L = Year Y W = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

DATE 24 JUL 2019

NOTES:

QFN15 4x4, 0.5P

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION b APPLIES TO THE PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 FROM THE TERMINAL TIP.
- 4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN.	NOM.	MAX.
А	0.80	0.85	0.90
A1	0.00		0.05
A3	0.10 REF		
b	0.20	0.25	0.30
D	3.95	4.00	4.05
Е	3.95	4.00	4.05
e	0.50 BSC		
L	0.30	0.40	0.50



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ■", may or may not be present. Some products may not follow the Generic Marking.

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 DESCRIPTION:
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